

Write-observation and Read-preservation TM Correctness Invariants (Appendix)

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1 Proof of Marking Theorem

For the sake of brevity, we use the shorthand notation

$$\exists l = o.n_T(v_1):v_2 \in X$$

for

$$\exists l \in X: obj_X(l) = o \wedge name_X(l) = n \wedge thread_X(l) = T \wedge arg1_X(l) = v_1 \wedge retv_X(l) = v_2$$

and similarly for universal quantification.

We also use W, R to denote labels.

Lemma 1. *For all $S \in TSequential$, $T \in S$, $S' = Visible(S, T)$, and $T', T'' \in S'$, we have $T' \preceq_{S'} T'' \iff T' \preceq_S T''$.*

Proof.

$$\begin{aligned} & T' \preceq_{S'} T'' \\ \iff & S'|T' \triangleleft_{S'} S'|T'' \vee T' = T'' \\ \iff & S|T' \triangleleft_{S'} S|T'' \vee T' = T'' \\ \iff & S|T' \triangleleft_S S|T'' \vee T' = T'' \\ \iff & T' \preceq_S T'' \end{aligned}$$

In these four steps we apply:

- 1) the definition of $\preceq_{S'}$,
- 2) that the definition of $Visible(S, T)$ implies both $S'|T' = S|T'$ and $S'|T'' = S|T''$,
- 3) $S' \in S$, and
- 4) the definition of \preceq_S . □

Lemma 2. For all $S \in TSequential$, $T \in S$, $i \in I$, $v, v' \in V$, $R = read_T(i):v \in GlobalReads(S)$, $S' = Visible(S, T)$, $T' \in S'$, and $W' = write_{T'}(i, v') \in GlobalWrites(S)$, we have

$$NoWriteBetween_{(S'|i)}(W', R) \iff NoWriterBetween_{S,i}(T', \preceq_S, T)$$

Proof.

$$\begin{aligned}
& NoWriteBetween_{(S'|i)}(W', R) \\
\iff & \forall W'' \in Writes(S'|i): W'' \preceq_{(S'|i)} W' \vee R \preceq_{(S'|i)} W'' \\
\iff & \forall T'' \in S'|i: \forall i' \in I: \forall v'' \in V: \forall W'' = write_{T''}(i', v'') \in S'|i: W'' \preceq_{(S'|i)} W' \vee R \preceq_{(S'|i)} W'' \\
\iff & \forall T'' \in S'|i: \forall v'' \in V: \forall W'' = write_{T''}(i, v'') \in S'|i: W'' \preceq_{(S'|i)} W' \vee R \preceq_{(S'|i)} W'' \\
\iff & \forall T'' \in S': \forall v'' \in V: \forall W'' = write_{T''}(i, v'') \in S': W'' \preceq_{S'} W' \vee R \preceq_{S'} W'' \\
\iff & \forall T'' \in S': \forall v'' \in V: \forall W'' = write_{T''}(i, v'') \in S': T'' \preceq_{S'} T' \vee T \preceq_{S'} T'' \\
\iff & \forall T'' \in S': \forall v'' \in V: \forall W'' = write_{T''}(i, v'') \in S': T'' \preceq_S T' \vee T \preceq_S T'' \\
\iff & \forall T'' \in S': \forall v'' \in V: \forall W'' = write_{T''}(i, v'') \in S': T'' \preceq_S T \Rightarrow T'' \preceq_S T' \\
\iff & \forall T'' \in S: \forall v'' \in V: \forall W'' = write_{T''}(i, v'') \in S: \\
& \quad [[(T'' = T) \vee (T'' \preceq_S T \wedge T'' \in Committed(S))] \wedge [T'' \preceq_S T]] \Rightarrow T'' \preceq_S T' \\
\iff & \forall T'' \in S: \forall v'' \in V: \forall W'' = write_{T''}(i, v'') \in S: \\
& \quad (T'' \in Committed(S) \wedge T'' \preceq_S T) \Rightarrow T'' \preceq_S T' \\
\iff & \forall T'' \in Writers_S(i): T'' \preceq_S T \Rightarrow T'' \preceq_S T' \\
\iff & \forall T'' \in Writers_S(i): T'' \preceq_S T' \vee T \preceq_S T'' \\
\iff & NoWriterBetween_{S,i}(T', \preceq_S, T)
\end{aligned}$$

In these twelve steps, we apply:

- 1) the definition of *NoWriteBetween*,
- 2) the definition of *Writes*,
- 3) the definition of projection $S'|i$,
- 4) R, W' and W'' access location i ,
- 5) $S' \in TSequential$ and $R \in GlobalReads(S')$ and $W' \in GlobalWrites(S')$ (that are concluded from $S \in TSequential$, $R \in GlobalReads(S)$, $W' \in GlobalWrites(S)$ and $S' = Visible(S, T)$.),
- 6) Lemma 1,
- 7) Boolean logic and that \preceq_S is total,
- 8) the definition of *Visible*,
- 9) logical simplification,
- 10) the definition of *Writers*,
- 11) Boolean logic and that \preceq_S is total, and
- 12) the definition of *NoWriterBetween*. □

Lemma 3. $TSequential \subset Sequential$

Proof. Straightforward from definitions of $TSequential$, $THistory$ and $Sequential$. \square

Lemma 4. $\forall i \in I: \forall v, v' \in V: \forall T, T' \in Trans: \text{if } R = read_T(i:v), W = write_{T'}(i,v), W' = write_T(i,v'), S \in TSequential, W \prec_S R, NoWriteBetween_S(W, R) \text{ and } W' \prec_S R, \text{ then } T = T'.$

Proof. Suppose (1) $S \in TSequential$, (2) $W \prec_S R$, (3) $NoWriteBetween_S(W, R)$ and (4) $W' \prec_S R$. From [1] and Lemma 3, we have (5) $S \in Sequential$. From [4] and [5], we have (6) $\neg(R \prec_S W')$. From [3] we have (7) $W' \preceq_S W \vee R \prec_S W'$. From [6] and [7], we have (8) $W' \preceq_S W$. From [2] and [8], we have (9) $W' \preceq_S W \preceq_S R$. From [9], [1], and that W' and R are by T and W is by T' , we have $T = T'$. \square

Lemma 5. *Suppose $S \in TSequential$. We have:*

$$\begin{aligned}
& \forall T \in S: \forall i \in I: \forall v \in V: \forall R = read_T(i):v \in LocalReads(S): \\
& \quad \exists T' \in Visible(S, T): \exists W = write_{T'}(i, v) \in Visible(S, T): \\
& \quad \quad W \prec_{(Visible(S, T) \mid i)} R \wedge NoWriteBetween_{(Visible(S, T) \mid i)}(W, R) \\
\iff & S \in LocalTSeqSpec
\end{aligned}$$

Proof. Suppose $S \in TSequential$. Thus, from Lemma 3, we have $S \in Sequential$. Let $S' = Visible(S, T)$. From $S \in TSequential$ and Lemma 1, we have $S' \in TSequential$. Thus, from Lemma 3, we have $S' \in Sequential$. From the definition of $Visible$, we have $S'|T = S|T$.

$$\begin{aligned}
& \forall T \in S: \forall i \in I: \forall v \in V: \forall R = read_T(i):v \in LocalReads(S): \\
& \quad \exists T' \in S': \exists W = write_{T'}(i, v) \in S': \\
& \quad \quad W \prec_{(S' \mid i)} R \wedge NoWriteBetween_{(S' \mid i)}(W, R) \\
\iff & \forall T \in S: \forall i \in I: \forall v \in V: \forall R = read_T(i):v \in LocalReads(S): \\
& \quad \exists v' \in V: \exists W' = write_T(i, v') \in S: W' \prec_S R \wedge \\
& \quad \exists T' \in S': \exists W = write_{T'}(i, v) \in S': \\
& \quad \quad W \prec_{(S' \mid i)} R \wedge NoWriteBetween_{(S' \mid i)}(W, R) \\
\iff & \forall T \in S: \forall i \in I: \forall v \in V: \forall R = read_T(i):v \in LocalReads(S): \\
& \quad \exists v' \in V: \exists W' = write_T(i, v') \in S': W' \prec_S R \wedge \\
& \quad \exists T' \in S': \exists W = write_{T'}(i, v) \in S': \\
& \quad \quad W \prec_{(S' \mid i)} R \wedge NoWriteBetween_{(S' \mid i)}(W, R) \\
\iff & \forall T \in S: \forall i \in I: \forall v \in V: \forall R = read_T(i):v \in LocalReads(S): \\
& \quad \exists v' \in V: \exists W' = write_T(i, v') \in S': W' \prec_{S'} R \wedge \\
& \quad \exists T' \in S': \exists W = write_{T'}(i, v) \in S': \\
& \quad \quad W \prec_{(S' \mid i)} R \wedge NoWriteBetween_{(S' \mid i)}(W, R) \\
\iff & \forall T \in S: \forall i \in I: \forall v \in V: \forall R = read_T(i):v \in LocalReads(S): \\
& \quad \exists v' \in V: \exists W' = write_T(i, v') \in S': W' \prec_{(S' \mid i)} R \wedge \\
& \quad \exists W = write_T(i, v) \in S': \\
& \quad \quad W \prec_{(S' \mid i)} R \wedge NoWriteBetween_{(S' \mid i)}(W, R) \\
\iff & \forall T \in S: \forall i \in I: \forall v \in V: \forall R = read_T(i):v \in LocalReads(S): \\
& \quad \exists W = write_T(i, v) \in S': \\
& \quad \quad W \prec_{(S' \mid i)} R \wedge NoWriteBetween_{(S' \mid i)}(W, R) \\
\iff & \forall T \in S: \forall i \in I: \forall v \in V: \forall R = read_T(i):v \in LocalReads(S): \\
& \quad \exists W = write_T(i, v) \in S: \\
& \quad \quad W \prec_{(S' \mid i)} R \wedge NoWriteBetween_{(S' \mid i)}(W, R)
\end{aligned}$$

$$\begin{aligned}
&\iff \forall T \in S: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i): v \in \text{LocalReads}(S): \\
&\quad \exists W = \text{write}_T(i, v) \in S: \\
&\quad \quad W \prec_{S'} R \wedge \text{NoWriteBetween}_{(S' | i)}(W, R) \\
&\iff \forall T \in S: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i): v \in \text{LocalReads}(S): \\
&\quad \exists W = \text{write}_T(i, v) \in S: \\
&\quad \quad W \prec_S R \wedge \text{NoWriteBetween}_{(S' | i)}(W, R) \\
&\iff \forall T \in S: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i): v \in \text{LocalReads}(S): \\
&\quad \exists W = \text{write}_T(i, v) \in S: \\
&\quad \quad W \prec_S R \wedge \forall W' \in \text{Writes}(S' | i): W' \preceq_{(S' | i)} W \vee R \prec_{(S' | i)} W' \\
&\iff \forall T \in S: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i): v \in \text{LocalReads}(S): \\
&\quad \exists W = \text{write}_T(i, v) \in S: \\
&\quad \quad W \prec_S R \wedge \neg \exists W' \in \text{Writes}(S' | i): \neg(W' \preceq_{(S' | i)} W) \wedge \neg(R \prec_{(S' | i)} W') \\
&\iff \forall T \in S: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i): v \in \text{LocalReads}(S): \\
&\quad \exists W = \text{write}_T(i, v) \in S: \\
&\quad \quad W \prec_S R \wedge \neg \exists W' \in \text{Writes}(S' | i): W \prec_{(S' | i)} W' \prec_{(S' | i)} R \\
&\iff \forall T \in S: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i): v \in \text{LocalReads}(S): \\
&\quad \exists W = \text{write}_T(i, v) \in S: \\
&\quad \quad W \prec_S R \wedge \neg \exists v' \in V: \exists W' = \text{write}_T(i, v'): W \prec_{(S' | i)} W' \prec_{(S' | i)} R \\
&\iff \forall T \in S: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i): v \in \text{LocalReads}(S): \\
&\quad \exists W = \text{write}_T(i, v) \in S: \\
&\quad \quad W \prec_S R \wedge \neg \exists v' \in V: \exists W' = \text{write}_T(i, v'): W \prec_{(S | i)} W' \prec_{(S | i)} R \\
&\iff \forall T \in S: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i): v \in \text{LocalReads}(S): \\
&\quad \exists W = \text{write}_T(i, v) \in S: \\
&\quad \quad W \prec_S R \wedge \neg \exists W' \in \text{Writes}(S | i): W \prec_{(S | i)} W' \prec_{(S | i)} R \\
&\iff \forall T \in S: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i): v \in \text{LocalReads}(S): \\
&\quad \exists W = \text{write}_T(i, v) \in S: \\
&\quad \quad W \prec_S R \wedge \forall W' \in \text{Writes}(S | i): \neg(W \prec_{(S | i)} W') \vee \neg(W' \prec_{(S | i)} R) \\
&\iff \forall T \in S: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i): v \in \text{LocalReads}(S): \\
&\quad \exists W = \text{write}_T(i, v) \in S: W \prec_S R \wedge \\
&\quad \quad \forall W' \in \text{Writes}(S | i): W' \preceq_{(S | i)} W \vee R \prec_{(S | i)} W' \\
&\iff \forall T \in S: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i): v \in \text{LocalReads}(S): \\
&\quad \exists W = \text{write}_T(i, v) \in S | T | i: W \prec_{S | T | i} R \wedge \\
&\quad \quad \forall W' \in \text{Writes}(S | T | i): W' \preceq_{(S | T | i)} W \vee R \prec_{(S | T | i)} W' \\
&\iff \forall T \in S: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i): v \in \text{LocalReads}(S): \\
&\quad \exists W = \text{write}_T(i, v) \in S | T | i: \\
&\quad \quad W \prec_{S | T | i} R \wedge \text{NoWriteBetween}_{(S | T | i)}(W, R) \\
&\iff S \in \text{LocalTSeqSpec}
\end{aligned}$$

In these twenty steps, we apply: 1) the definition of *LocalReads*,

- 2) the definition of *Visible*,
- 3) $S'|T = S|T$ and that both W' and R are by T ,
- 4) that both W' and R are on i ,
- 5) Lemma 4,
- 6) duplicate conjunction,
- 7) the definition of *Visible*,
- 8) that both R and W are on i ,
- 9) $S'|T = S|T$ and that both R and W are by T ,
- 10) the definition of *NoWriteBetween*,
- 11) first-order logic,
- 12) $(S' | i) \in \textit{Sequential}$,
- 13) from $(S' | i) \in \textit{TSequential}$, R and W are by transaction T and W' is between them, we have W' is by T ,
- 14) $S'|T = S|T$,
- 15) from $(S | i) \in \textit{TSequential}$, R and W are by transaction T and W' is between them, we have W' is by T .
- 16) first-order logic,
- 17) $(S | i) \in \textit{Sequential}$,
- 18) $(S | i) \in \textit{Sequential}$, $\textit{thread}_H(R) = \textit{thread}_H(W) = T$ and $\textit{arg1}_H(R) = \textit{arg1}_H(W) = i$,
- 19) the definition of *NoWriteBetween*,
- 20) the definition of *LocalTSeqSpec*.

□

Lemma 6. *Suppose $S \in TSequential \cap TComplete$. We have:*

$$\begin{aligned}
& S \in TSeqSpec \\
\iff & S \in LocalTSeqSpec \wedge \\
& \forall T \in S: \forall i \in I: \forall v \in V: \forall R = read_T(i): v \in GlobalReads(S): \\
& \exists T' \in Committed(S): \exists W = write_{T'}(i, v) \in GlobalWrites(S): \\
& (T' \ll_S T) \wedge NoWriterBetween_{S,i}(T', \ll_S T)
\end{aligned}$$

Proof. Suppose $S \in TSequential \cap TComplete$. From $S \in TSequential$ and Lemma 1, we have $Visible(S, T) \in TSequential$.

$$\begin{aligned}
& S \in TSeqSpec \\
\iff & \forall T \in S: \forall i \in I: (Visible(S, T) \mid i) \in SeqSpec(i) \\
\iff & \forall T \in S: \forall i \in I: \\
& \quad \forall T'' \in (Visible(S, T) \mid i): \forall v \in V: \forall R = read_{T''}(i): v \in (Visible(S, T) \mid i): \\
& \quad \exists T' \in (Visible(S, T) \mid i): \exists W = write_{T'}(i, v) \in (Visible(S, T) \mid i): \\
& \quad W \prec_{(Visible(S, T) \mid i)} R \wedge NoWriteBetween_{(Visible(S, T) \mid i)}(W, R) \\
\iff & \forall T \in S: \forall i \in I: \\
& \quad \forall T'' \in Visible(S, T): \forall v \in V: \forall R = read_{T''}(i): v \in Visible(S, T): \\
& \quad \exists T' \in Visible(S, T): \exists W = write_{T'}(i, v) \in Visible(S, T): \\
& \quad W \prec_{(Visible(S, T) \mid i)} R \wedge NoWriteBetween_{(Visible(S, T) \mid i)}(W, R) \\
\iff & \forall T \in S: \forall i \in I: \forall v \in V: \forall R = read_T(i): v \in S: \\
& \quad \exists T' \in Visible(S, T): \exists W = write_{T'}(i, v) \in Visible(S, T): \\
& \quad W \prec_{(Visible(S, T) \mid i)} R \wedge NoWriteBetween_{(Visible(S, T) \mid i)}(W, R) \\
\iff & \forall T \in S: \forall i \in I: \forall v \in V: \forall R = read_T(i): v \in LocalReads(S): \\
& \quad \exists T' \in Visible(S, T): \exists W = write_{T'}(i, v) \in Visible(S, T): \\
& \quad W \prec_{(Visible(S, T) \mid i)} R \wedge NoWriteBetween_{(Visible(S, T) \mid i)}(W, R) \\
& \wedge \\
& \forall T \in S: \forall i \in I: \forall v \in V: \forall R = read_T(i): v \in GlobalReads(S): \\
& \quad \exists T' \in Visible(S, T): \exists W = write_{T'}(i, v) \in Visible(S, T): \\
& \quad W \prec_{(Visible(S, T) \mid i)} R \wedge NoWriteBetween_{(Visible(S, T) \mid i)}(W, R) \\
\iff & S \in LocalTSeqSpec \wedge \\
& \forall T \in S: \forall i \in I: \forall v \in V: \forall R = read_T(i): v \in GlobalReads(S): \\
& \quad \exists T' \in Visible(S, T): \exists W = write_{T'}(i, v) \in Visible(S, T): \\
& \quad W \prec_{(Visible(S, T) \mid i)} R \wedge NoWriteBetween_{(Visible(S, T) \mid i)}(W, R)
\end{aligned}$$

$$\begin{aligned}
&\iff S \in \text{LocalTSeqSpec} \wedge \\
&\quad \forall T \in S: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i):v \in \text{GlobalReads}(S): \\
&\quad \quad \exists T' \in \text{Visible}(S, T): \exists W = \text{write}_{T'}(i, v) \in \text{Visible}(S, T): \\
&\quad \quad \quad W \prec_{\text{Visible}(S, T)} R \wedge \text{NoWriteBetween}_{(\text{Visible}(S, T) \mid i)}(W, R) \\
&\iff S \in \text{LocalTSeqSpec} \wedge \\
&\quad \forall T \in S: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i):v \in \text{GlobalReads}(S): \\
&\quad \quad \exists T' \in \text{Visible}(S, T): \exists W = \text{write}_{T'}(i, v) \in \text{Visible}(S, T): \\
&\quad \quad \quad T' \prec_{\text{Visible}(S, T)} T \wedge \text{NoWriteBetween}_{(\text{Visible}(S, T) \mid i)}(W, R) \\
&\iff S \in \text{LocalTSeqSpec} \wedge \\
&\quad \forall T \in S: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i):v \in \text{GlobalReads}(S): \\
&\quad \quad \exists T' \in \text{Visible}(S, T): \exists W = \text{write}_{T'}(i, v) \in \text{Visible}(S, T): \\
&\quad \quad \quad T' \prec_S T \wedge \text{NoWriteBetween}_{(\text{Visible}(S, T) \mid i)}(W, R) \\
&\iff S \in \text{LocalTSeqSpec} \wedge \\
&\quad \forall T \in S: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i):v \in \text{GlobalReads}(S): \\
&\quad \quad \exists T' \in \text{Visible}(S, T): \exists W = \text{write}_{T'}(i, v) \in \text{GlobalWrites}(S): \\
&\quad \quad \quad T' \prec_S T \wedge \text{NoWriteBetween}_{(\text{Visible}(S, T) \mid i)}(W, R) \\
&\iff S \in \text{LocalTSeqSpec} \wedge \\
&\quad \forall T \in S: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i):v \in \text{GlobalReads}(S): \\
&\quad \quad \exists T' \in \text{Visible}(S, T): \exists W = \text{write}_{T'}(i, v) \in \text{GlobalWrites}(S): \\
&\quad \quad \quad T' \prec_S T \wedge \text{NoWriterBetween}_{S, i}(T', \prec_S, T) \\
&\iff S \in \text{LocalTSeqSpec} \wedge \\
&\quad \forall T \in S: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i):v \in \text{GlobalReads}(S): \\
&\quad \quad \exists T' \in \text{Visible}(S, T): \exists W = \text{write}_{T'}(i, v) \in \text{GlobalWrites}(S): \\
&\quad \quad \quad (T' \prec_S T) \wedge T' \in \text{Committed}(S) \wedge \text{NoWriterBetween}_{S, i}(T', \preceq_S, T) \\
&\iff S \in \text{LocalTSeqSpec} \wedge \\
&\quad \forall T \in S: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i):v \in \text{GlobalReads}(S): \\
&\quad \quad \exists T' \in \text{Committed}(S): \exists W = \text{write}_{T'}(i, v) \in \text{GlobalWrites}(S): \\
&\quad \quad \quad (T' \prec_S T) \wedge \text{NoWriterBetween}_{S, i}(T', \preceq_S, T)
\end{aligned}$$

In these thirteen steps, we apply:

- 1) the definition of $TSeqSpec$ and $S \in TSequential \cap TComplete$,
- 2) the definition of $SeqSpec(i)$,
- 3) R and W access location i ,
- 4) that we can choose $T'' = T$,
- 5) $Reads(S) = LocalReads(S) \cup GlobalReads(S)$,
- 6) Lemma 5,
- 7) that R and W are both on location i
- 8) that R and W are by transactions T and T' respectively, $Visible(S, T) \in TSequential$, and $R \in GlobalReads(Visible(S, T))$ (because $R \in GlobalReads(R)$ and $Visible(S, T) \mid T = S \mid T$),
- 9) Lemma 1,
- 10) $T' \prec_S T$ and $\text{NoWriteBetween}_{(\text{Visible}(S, T) \mid i)}(W, R)$,
- 11) Lemma 2,

- 12) $T' \in \text{Visible}(S, T)$ and $(T' \prec_S T)$, and
- 13) the definition of $\text{Visible}(S, T)$.

□

Lemma 7. (Invariance) *If $H \equiv H'$, then $\text{Marking}(H) = \text{Marking}(H')$ and $\text{ReadPres}(H) = \text{ReadPres}(H')$ and $\text{WriteObs}(H) = \text{WriteObs}(H')$.*

Proof. Immediate from the definitions of Marking , ReadPres , and WriteObs . □

Lemma 8. $\forall H \in \text{THistory}: \forall \sqsubseteq \in \text{Marking}(H): \exists S \in \text{TSequential}: H \equiv S \wedge \preceq_H \subseteq \preceq_S \wedge \preceq_S \subseteq \sqsubseteq$.

Proof. Let $H \in \text{THistory}$ and let $\sqsubseteq \in \text{Marking}(H)$. We have that \sqsubseteq is a total order of Trans so we can choose a permutation π on $1..n$ such that $\forall i, j \in 1..n: (i < j) \Leftrightarrow (T_{\pi(i)} \sqsubseteq T_{\pi(j)})$. Define: $S = H|_{T_{\pi(1)}}, \dots, H|_{T_{\pi(n)}}$. It is straightforward to prove that $S \in \text{TSequential} \wedge H \equiv S \wedge \preceq_H \subseteq \preceq_S \wedge \preceq_S \subseteq \sqsubseteq$. □

Lemma 9. *Suppose $\sqsubseteq \in \text{Marking}(H) \wedge p_2 \notin \text{Writers}_H(i)$.*

If $\text{NoWriterBetween}_{H,i}(T_1, \sqsubseteq, p_2)$ and $\text{NoWriterBetween}_{H,i}(p_2, \sqsubseteq, T_3)$, then $\text{NoWriterBetween}_{H,i}(T_1, \sqsubseteq, T_3)$.

Proof.

$$\begin{aligned}
& \text{NoWriterBetween}_{H,i}(T_1, \sqsubseteq, p_2) \wedge \text{NoWriterBetween}_{H,i}(p_2, \sqsubseteq, T_3) \\
\iff & \forall T \in \text{Writers}_H(i): (T \sqsubseteq T_1 \vee p_2 \sqsubseteq T) \wedge (T \sqsubseteq p_2 \vee T_3 \sqsubseteq T) \\
\iff & \forall T \in \text{Writers}_H(i): (T \sqsubseteq T_1 \wedge (T \sqsubseteq p_2 \vee T_3 \sqsubseteq T)) \vee \\
& \quad (p_2 \sqsubseteq T \wedge T \sqsubseteq p_2) \vee (p_2 \sqsubseteq T \wedge T_3 \sqsubseteq T) \\
\implies & \forall T \in \text{Writers}_H(i): (T \sqsubseteq T_1) \vee (T_3 \sqsubseteq T) \\
\iff & \text{NoWriterBetween}_{H,i}(T_1, \sqsubseteq, T_3)
\end{aligned}$$

The first step uses the definition of NoWriterBetween . The second step uses \wedge distribution over \vee . The third step simplifies the first disjunct using conjunction elimination, eliminates the second disjunct using $p_2 \notin \text{Writers}_H(i)$ and simplifies the third disjunct using conjunction elimination. The fourth step uses the definition of NoWriterBetween . □

Lemma 10. *Suppose $S \in TSequential \cap TComplete$. We have:*

$$S \in TSeqSpec \iff S \in Markable$$

Proof. Let $S \in TSequential \cap TComplete$. From Lemma 6, the definition of *Markable*, and $S \in TComplete$, we have that we must prove:

$$\begin{aligned} & S \in LocalTSeqSpec \wedge \\ & \forall T \in S: \forall i \in I: \forall v \in V: \forall R = read_T(i):v \in GlobalReads(S): \\ & \exists T' \in Committed(S): \exists W = write_{T'}(i, v) \in GlobalWrites(S): \\ & \quad (T' \ll_S T) \wedge NoWriterBetween_{S,i}(T', \ll_S T) \\ \iff & \exists \sqsubseteq \in Marking(S): \ll_S \subseteq \sqsubseteq \wedge \sqsubseteq \in ReadPres(S) \wedge \sqsubseteq \in WriteObs(S) \end{aligned}$$

From the definition of *WriteObs* and *LastPreAccessor* we have that:

$$\begin{aligned} & \sqsubseteq \in WriteObs(S) \\ \iff & S \in LocalTSeqSpec \wedge \\ & \forall T \in Trans: \forall i \in I: \forall v \in V: \forall R = read_T(i):v \in GlobalReads(S): \\ & \exists T' \in Trans: \exists W = write_{T'}(i, v) \in GlobalWrites(S): \\ & \quad T' \in Writers_S(i) \wedge T' \neq T \wedge T' \sqsubset R \wedge NoWriterBetween_{S,i}(T', \sqsubseteq, R) \\ \iff & S \in LocalTSeqSpec \wedge \\ & \forall T \in Trans: \forall i \in I: \forall v \in V: \forall R = read_T(i):v \in GlobalReads(S): \\ & \exists T' \in Trans: \exists W = write_{T'}(i, v) \in GlobalWrites(S): \\ & \quad T' \in Committed(S) \wedge T' \neq T \wedge T' \sqsubset R \wedge NoWriterBetween_{S,i}(T', \sqsubseteq, R) \end{aligned}$$

We are now ready to prove the two directions of the equivalence.

\Rightarrow :

Assume that

$$\begin{aligned} & S \in LocalTSeqSpec \wedge \\ & \forall T \in S: \forall i \in I: \forall v \in V: \forall R = read_T(i):v \in GlobalReads(S): \\ & \exists T' \in Committed(S): \exists W = write_{T'}(i, v) \in GlobalWrites(S): \\ & \quad (T' \ll_S T) \wedge NoWriterBetween_{S,i}(T', \ll_S T) \end{aligned}$$

Define:

$$\begin{aligned} p_1 \sqsubset p_2 & \iff (p_1 \ll_S p_2) \vee \\ & \quad (thread_S(p_1) \ll_S p_2) \vee \\ & \quad (p_1 \ll_S thread_S(p_2)) \\ p_1 \sqsubseteq p_2 & \iff p_1 \sqsubset \vee p_2 p_1 = p_2 \end{aligned}$$

We show that

$$\begin{aligned} & \sqsubseteq \in Marking(S) \wedge \\ & \ll_S \subseteq \sqsubseteq \wedge \sqsubseteq \in ReadPres(S) \wedge \\ & S \in LocalTSeqSpec \wedge \\ & \forall T \in Trans: \forall i \in I: \forall v \in V: \forall R = read_T(i):v \in GlobalReads(S): \\ & \exists T' \in Trans: \exists W = write_{T'}(i, v) \in GlobalWrites(S): \\ & \quad T' \in Committed(S) \wedge T' \neq T \wedge T' \sqsubset R \wedge NoWriterBetween_{S,i}(T', \sqsubseteq, R) \end{aligned}$$

It is straightforward to prove $\sqsubseteq \in \text{Marking}(S)$ and $\preceq_S \subseteq \sqsubseteq$, $\sqsubseteq \in \text{ReadPres}(S)$. Additionally, the first conjunct of $\text{WriteObs}(S)$ (that is, $S \in \text{LocalTSeqSpec}$) is immediate from the assumption. So, we still need to prove the second conjunct of $\text{WriteObs}(S)$.

Let $T \in \text{Trans}$, $i \in I$, $v \in V$, $R = \text{read}_T(i):v \in \text{GlobalReads}(S)$. From the assumption (the left-hand side), we have that we can find (1) $T' \in \text{Committed}(S)$ and (2) $W = \text{write}_{T'}(i,v) \in \text{GlobalWrites}(S)$ such that (3) $(T' \prec_S T)$ and (4) $\text{NoWriterBetween}_{S,i}(T', \preceq_S, T)$. Let us now prove each conjunct of $T' \neq T \wedge T' \sqsubseteq R \wedge \text{NoWriterBetween}_{S,i}(T', \sqsubseteq, R)$ in turn.

From [3] and that \preceq_S is a total order of $\text{Trans}(S)$, we have (5) $T' \neq T$. From [3] and the definition of \sqsubseteq , we have $T' \sqsubseteq R$. From [4] and $\preceq_S \subseteq \sqsubseteq$, we have (6) $\text{NoWriterBetween}_{S,i}(T', \sqsubseteq, T)$. From $T \preceq_S T$ and the definition of \sqsubseteq , we have (7) $R \sqsubseteq T$. From [6], [7] and the definition of \sqsubseteq and transitivity of \preceq_S , we have $\text{NoWriterBetween}_{S,i}(T', \sqsubseteq, R)$.

\Leftarrow :

Assume the right-hand side and choose $\sqsubseteq \in \text{Marking}(S)$ such that:

$$\begin{aligned} & \preceq_S \subseteq \sqsubseteq \quad \wedge \quad \sqsubseteq \in \text{ReadPres}(S) \quad \wedge \\ & S \in \text{TLocalSeqSpec} \quad \wedge \\ & \forall T \in \text{Trans}: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i):v \in \text{GlobalReads}(S): \\ & \exists T' \in \text{Committed}(S): \exists W = \text{write}_{T'}(i,v) \in \text{GlobalWrites}(S): \\ & \quad T' \neq T \quad \wedge \quad T' \sqsubseteq R \quad \wedge \quad \text{NoWriterBetween}_{S,i}(T', \sqsubseteq, R) \end{aligned}$$

We show that

$$\begin{aligned} & S \in \text{LocalTSeqSpec} \quad \wedge \\ & \forall T \in S: \forall i \in I: \forall v \in V: \forall R = \text{read}_T(i):v \in \text{GlobalReads}(S): \\ & \exists T' \in \text{Committed}(S): \exists W = \text{write}_{T'}(i,v) \in \text{GlobalWrites}(S): \\ & \quad (T' \prec_S T) \quad \wedge \quad \text{NoWriterBetween}_{S,i}(T', \preceq_S, T) \end{aligned}$$

The first conjunct (of the left-hand side), $S \in \text{LocalTSeqSpec}$, is immediate from the assumption. From the assumption we have (1) $\preceq_S \subseteq \sqsubseteq$, (2) $\sqsubseteq \in \text{ReadPres}(S)$. Let $T \in \text{Trans}$, $i \in I$, $v \in V$, $R = \text{read}_T(i):v \in \text{GlobalReads}(S)$. From the above property of \sqsubseteq , we have that we can find (3) $T' \in \text{Committed}(S)$ and (4) $W = \text{write}_{T'}(i,v) \in \text{GlobalWrites}(S)$ such that (5) $T' \neq T$ and (6) $T' \sqsubseteq R$ and (7) $\text{NoWriterBetween}_{S,i}(T', \sqsubseteq, R)$. From [1], that \sqsubseteq is a total order on $\text{Trans}(S)$ ($\sqsubseteq \in \text{Marking}(S)$), and that \preceq_S is a total order on $\text{Trans}(S)$ ($S \in \text{TSequential}$), we have (8) $\forall T, T' \in \text{Trans}: T' \sqsubseteq T \Rightarrow T' \preceq_S T$.

First we prove $T' \prec_S T$. From [2], we have (9) $\text{NoWriterBetween}_{S,i}(T, \sqsubseteq, R)$. From [3] and [4], we have (10) $T' \in \text{Writers}_S(i)$. From [9] and [10], we have (11) $T' \sqsubseteq T \vee R \sqsubseteq T'$. From [6], $T' \neq R$ and \sqsubseteq is a total order on $\{R\} \cup \text{Writers}_S(i)$ ($\sqsubseteq \in \text{Marking}(S)$), we have (12) $R \not\sqsubseteq T'$. From [11] and [12], we have (13) $T' \sqsubseteq T$. From [8] and [13], we have (14) $T' \preceq_S T$. From [14] and [5], we have $T' \prec_S T$.

Second, we prove $\text{NoWriterBetween}_{S,i}(T', \preceq_S, T)$. From [2], we have (15) $\text{NoWriterBetween}_{S,i}(R, \sqsubseteq, T)$. From $R \notin \text{Writers}_S(i)$, [7], [15], and Lemma 9, we have (16) $\text{NoWriterBetween}_{S,i}(T', \sqsubseteq, T)$. From [16] and [8] we have $\text{NoWriterBetween}_{S,i}(T', \preceq_S, T)$. \square

Theorem (Marking) $FinalStateOpaque = Markable$.

Proof.

$$\begin{aligned}
& FinalStateOpaque \\
= & \{H \in THistory \mid \exists H' \in TExtension(H): \exists S \in TSequential: \\
& \quad H' \equiv S \wedge \preceq_{H'} \subseteq \preceq_S \wedge S \in TSeqSpec\} \\
= & \{H \in THistory \mid \exists H' \in TExtension(H): \exists S \in TSequential: \\
& \quad H' \equiv S \wedge \preceq_{H'} \subseteq \preceq_S \wedge S \in Markable\} \\
= & \{H \in THistory \mid \exists H' \in TExtension(H): \exists S \in TSequential: H' \equiv S \wedge \preceq_{H'} \subseteq \preceq_S \wedge \\
& \quad \exists \sqsubseteq \in Marking(S): \preceq_S \subseteq \sqsubseteq \wedge \sqsubseteq \in ReadPres(S) \cap WriteObs(S)\} \\
= & \{H \in THistory \mid \exists H' \in TExtension(H): \exists S \in TSequential: H' \equiv S \wedge \preceq_{H'} \subseteq \preceq_S \wedge \\
& \quad \exists \sqsubseteq \in Marking(H'): \preceq_S \subseteq \sqsubseteq \wedge \sqsubseteq \in ReadPres(H') \cap WriteObs(H')\} \\
= & \{H \in THistory \mid \exists H' \in TExtension(H): \exists \sqsubseteq \in Marking(H'): \\
& \quad \sqsubseteq \in ReadPres(H') \cap WriteObs(H') \wedge \\
& \quad \exists S \in TSequential: H' \equiv S \wedge \preceq_{H'} \subseteq \preceq_S \wedge \preceq_S \subseteq \sqsubseteq \} \\
= & \{H \in THistory \mid \exists H' \in TExtension(H): \exists \sqsubseteq \in Marking(H'): \\
& \quad \preceq_{H'} \subseteq \sqsubseteq \wedge \sqsubseteq \in ReadPres(H') \cap WriteObs(H') \wedge \\
& \quad \exists S \in TSequential: H' \equiv S \wedge \preceq_{H'} \subseteq \preceq_S \wedge \preceq_S \subseteq \sqsubseteq \} \\
= & \{H \in THistory \mid \exists H' \in TExtension(H): \exists \sqsubseteq \in Marking(H'): \\
& \quad \preceq_{H'} \subseteq \sqsubseteq \wedge \sqsubseteq \in ReadPres(H') \cap WriteObs(H')\} \\
= & Markable
\end{aligned}$$

In these eight steps we apply:

- 1) the definition of $FinalStateOpaque$,
- 2) Lemma 10 and $S \in TComplete$ (because $H' \in TExtension(H)$ and $H' \equiv S$),
- 3) the definition of $Markable$ and $S \in TComplete$,
- 4) Lemma 7,
- 5) logical rearrangement,
- 6) transitivity of \subseteq ,
- 7) Lemma 8, and
- 8) the definition of $Markable$. □

2 TL2 Marking

<p>Shared objects:</p> <p>r: <i>SafeReg</i>[I], initially \perp ver: <i>AtomicReg</i>[I], initially 0 $lock$: <i>TryLock</i>[I], initially \mathbb{R} $clock$: <i>SCounter</i>, initially 0</p>	<p>Thread-local objects: For each $T \in Trans$:</p> <p>$rver_T$: <i>SafeReg</i>, initially \perp $rset_T$: <i>BasicSet</i>, initially \emptyset $wset_T$: <i>BasicMap</i>, initially \emptyset</p>
<p>$R01$: def $read_T(i)$ $R02$: if ($rver_T = \perp$) $R03$: $snap := clock.read()$ $R04$: $rver_T.write(snap)$</p> <p>$R05$: if ($i \in dom(wset_T)$) $R06$: return $wset_T(i)$</p> <p>$R07$: $t := ver[i].read()$ $R08$: $v := reg[i].read()$ $R09$: $l := lock[i].read()$ $R10$: $t' := ver[i].read()$ $R11$: if ($\neg(l = false \wedge t = t' \wedge t' \leq rver_T)$) $R12$: return A</p> <p>$R13$: $rver_T.add(i)$ $R14$: return v</p>	<p>$C01$: def $commit_T$ $C02$: foreach ($i \in dom(wset_T)$) $C03$: $locked := lock[i].trylock()$ $C04$: if ($locked$) $C05$: $lset.add(i)$ $C06$: else $C07$: foreach ($i \in lset$) $lock[i].unlock()$ $C08$: return A</p> <p>$C09$: $wver := clock.iaf$ $C10$: if ($wver \neq rver_T + 1$) $C11$: foreach ($i \in rset_T$) $C12$: $l := lock[i].read()$ $C13$: $t := ver[i].read()$ $C14$: if ($\neg(l = false \wedge t \leq rver_T)$) $C15$: foreach ($i \in lset$) $lock[i].unlock()$ $C16$: return A</p>
<p>$W01$: def $write_T(i, v)$ $W02$: $wset_T.put(i \mapsto v)$ $W03$: return ok</p>	<p>$C17$: foreach ($(i \mapsto v) \in wset_T$) $C18$: $reg[i].write(v)$ $C19$: $ver[i].write(wver)$ $C20$: $lock[i].unlock()$</p> <p>$C21$: return C</p>
<p>In addition to the orders imposed by the data and control dependencies and lock synchronization, the following orders are required: $R06 \prec R07$, $R07 \prec R08$, $R08 \prec R09$, $C12 \prec C13$, $C18 \prec C19$</p>	

Figure 1: TL2 Algorithm

Consider an execution history X of TL2 such that $H = X|_{mem}$ and $H \in TComplete$. Let

$$\begin{aligned}
readAcc(R) &= R08 \text{ in } R \\
writeAcc(T, i) &= C18 \text{ for } i \text{ in } Commit_T \\
Eff(T) &= \begin{cases} R03 \text{ (in the first read of } T) & \text{if } T \in Aborted(H) \\ C09 \text{ (in } commit_T) & \text{if } T \in Committed(H) \end{cases}
\end{aligned}$$

Let \prec_{clock} represent the linearization order of the strong counter $clock$. The marking \sqsubseteq for H is the reflexive closure of \sqsubset that is define as follows:

$$\begin{aligned}
&Let\ T, T' \in Trans(H): \\
&\quad T \sqsubset T' \Leftrightarrow Eff(T) \prec_{clock} Eff(T') \\
&Let\ R \in Reads(H), i = arg1(R), T \in Writers_H(i): \\
&\quad T \sqsubset R \Leftrightarrow writeAcc(T, i) \prec_X readAcc(R) \\
&\quad R \sqsubset T \Leftrightarrow readAcc(R) \prec_X writeAcc(T, i)
\end{aligned}$$

Figure 2: The marking of TL2.

The marking relation for TL2 is defined in Figure 2. The effect order of transactions is the linearization order of their calls to the $clock$ strong counter. The access order of read operations and writer transactions to location i is the execution order of their access to the $reg[i]$ register.

3 DSTM (visible reads) Marking

$Loc \{writer: SafeReg, rset: BasicSet, oldVal: SafeReg, newVal: SafeReg\}$ Shared objects: $state: CASReg[Trans]$, initially \mathbb{R} $ref: CASReg[I]$, initially $new Loc(T_0, \emptyset, 0, 0)$	
$R01 : \mathbf{def} \text{ read}_T(i)$ $R02 : r := ref[i].read()$ $R03 : v := currentValue_T(r)$ $R04 : r' = r.clone()$ $R05 : r'.rset.add(T)$ $R06 : b := ref[i].cas(r, r')$ $R07 : s := state_T.read()$ $R08 : \mathbf{if} (\neg b \vee (s = \mathbb{A}))$ $R09 : \quad \mathbf{return} A$ $R10 : \mathbf{else}$ $R11 : \quad \mathbf{return} v$	$W01 : \mathbf{def} \text{ write}_T(i, v)$ $W02 : r := ref[i].read()$ $W04 : w := r.writer.read()$ $W05 : \mathbf{if} (w = T)$ $W06 : \quad r.newVal.write(v)$ $W07 : \quad \mathbf{return} ok$ $W08 : v' := currentValue_T(r)$ $W09 : \mathbf{foreach} (T' \in r.rset)$ $W10 : \quad state_{T'}.cas(\mathbb{R}, \mathbb{A})$ $W11 : r' := new Loc(T, \emptyset, v', v)$ $W12 : b := ref[i].cas(r, r')$ $W13 : \mathbf{if} (b)$ $W14 : \quad \mathbf{return} ok$ $W15 : \mathbf{else}$ $W16 : \quad \mathbf{return} A$
$C01 : \mathbf{def} \text{ commit}_T()$ $C02 : b := state_T.cas(\mathbb{R}, \mathbb{C})$ $C03 : \mathbf{if} (b)$ $C04 : \quad \mathbf{return} C$ $C05 : \mathbf{else}$ $C06 : \quad \mathbf{return} A$	
$V01 : \mathbf{def} \text{ currentValue}_T(r)$ $V02 : T' = r.writer.read()$ $V04 : \mathbf{if} (\neg(T' = T))$ $V05 : \quad state_{T'}.cas(\mathbb{R}, A)$ $V06 : s := state_{T'}.read()$ $V07 : \mathbf{if} (s = \mathbb{A})$ $V08 : \quad \mathbf{return} r.oldVal$ $V09 : \mathbf{else}$ $V10 : \quad \mathbf{return} r.newVal$	

Figure 3: DSTM (visible reads) Algorithm

Consider an execution history X of DSTM such that $H = X|_{mem}$ and $H \in TComplete$. Let

$$\begin{aligned}
readAcc(R) &= R06 \text{ in } R \\
writeAcc(T, i) &= W12 \text{ in the first write to } i \text{ by } T \\
Eff(T) &= \begin{cases} C02 \text{ of the commit operation} & \text{if } T \text{ is committed} \\ R06 \text{ of the last successful read} & \text{if } T \text{ is aborted and has a successful read} \\ \text{Any point in } T & \text{if } T \text{ is aborted and has no successful read} \end{cases}
\end{aligned}$$

Let $\prec_{ref[i]}$ represent the linearization order of $ref[i]$. The marking \sqsubseteq for H is the reflexive closure of \sqsubset that is define as follows:

$$\begin{aligned}
&Let T, T' \in Trans(H): \\
&\quad T \sqsubset T' \Leftrightarrow Eff(T) \prec_X Eff(T') \\
&Let R \in Reads(H), i = arg1(R), T \in Writers_H(i): \\
&\quad T \sqsubset R \Leftrightarrow writeAcc(T, i) \prec_{ref[i]} readAcc(R) \\
&\quad R \sqsubset T \Leftrightarrow readAcc(R) \prec_{ref[i]} writeAcc(T, i)
\end{aligned}$$

Figure 4: The marking of DSTM (visible reads).

The marking relation for DSTM (visible reads) is defined in Figure 4.

Committed transactions take effect at the final *cas* of their state from \mathbb{R} to \mathbb{C} , $C02$, of their commit operation. Aborted transactions that have successful read operations take effect at state check, $R06$, of their last successful read.

The access order of read operations and writer transactions to location i is the linearization order of their *cas* calls to the $ref[i]$ register.

4 Opacity

$$\begin{aligned}
Reads(H) &= \{R \mid R \in H \wedge obj_H(R) = this \wedge \\
&\quad name_H(R) = read \wedge retv_H(R) \neq \mathbb{A}\} \\
Writes(H) &= \{W \mid W \in H \wedge obj_H(W) = this \wedge \\
&\quad name_H(W) = write \wedge retv_H(W) \neq \mathbb{A}\} \\
Trans(H) &= \{T \mid \exists l \in H: thread_H(l) = T\} \\
TSequential &= \{S \in THistory \mid \preceq_S \text{ is a total order of } Trans(S)\} \\
Committed(H) &= \{T \mid \exists l \in H: thread_H(l) = T \wedge retv_H(l) = \mathbb{C}\} \\
Aborted(H) &= \{T \mid \exists l \in H: thread_H(l) = T \wedge retv_H(l) = \mathbb{A}\} \\
Completed(H) &= Committed(H) \cup Aborted(H) \\
Live(H) &= Trans(H) \setminus Completed(H) \\
TComplete &= \{H \in THistory \mid \forall T \in Trans(H): T \in Completed(H)\} \\
CommitPending(H) &= \{T \in Live(H) \mid \exists l \in H: thread_H(l) = T \wedge name_H(l) = commit \\
&\quad iEv(l) \in H \wedge \neg(rEv(l) \in H)\} \\
TExtension(H) &= \{H' \in THistory \mid H \text{ is a prefix of } H' \wedge \forall T \in Trans(H') \Rightarrow T \in Trans(H) \wedge \\
&\quad Live(H) \setminus CommitPending(H) \subseteq Aborted(H') \wedge \\
&\quad CommitPending(H) \subseteq Completed(H')\} \\
Visible(S, T) &= filter(S, \lambda T'. (T' = T) \vee ((T' \ll_S T) \wedge T' \in Committed(S))) \\
NoWriteBetween_S(W, R) &= \forall W' \in Writes(S): W' \preceq_S W \vee R \prec_S W' \\
SeqSpec(i) &= \{S \in Sequential \mid \forall R \in Reads(S): \exists W \in Writes(S): \\
&\quad W \prec_S R \wedge NoWriteBetween_S(W, R) \wedge \\
&\quad retv_S(R) = arg2_S(W)\} \\
TSeqSpec &= \{S \in TSequential \cap TComplete \mid \forall T \in S: \forall i \in I: \\
&\quad (Visible(S, T) \mid i) \in SeqSpec(i)\} \\
FinalStateOpaque &= \{H \in THistory \mid \exists H' \in TExtension(H): \exists S \in TSeqSpec: \\
&\quad H' \equiv S \wedge \preceq_{H'} \subseteq \preceq_S \wedge S \in TSeqSpec\}
\end{aligned}$$

Figure 5: *FinalStateOpaque*

Opacity of a TM algorithm is defined in two steps. First, it is defined what it means for a transaction history to be opaque which is called final-state-opacity. Then, a TM algorithm is defined to be opaque if every transaction history of every source program running on top of that TM algorithm is final-state-opaque.

FinalStateOpaque is defined in Figure 5. We use T prefix before some of the terms to avoid confusion with the terms that we defined above for execution histories of objects. We say that a transaction history is sequential if it is a sequence of transactions. A transaction T is committed or aborted in a transaction history H if there is respectively a commit or abort response event for T in H . A completed transaction is either committed or aborted. A live transaction is a transaction that is not completed. A transaction history is complete if all its transactions are completed. A pending transaction has a pending event and a commit-pending transaction has a commit pending event. An extension of a history is obtained by committing or

aborting its commit-pending transactions and aborting the other live transactions. If H is a transaction history and p is a predicate on transaction identifiers, we define $filter(H, p)$ to be the subsequence of H that contains the events of transactions T for which $p(T)$ is true. The visible history for a transaction T in a sequential transaction history S , $Visible(S, T)$, is the sequence of committed transactions before T in S and T itself. The sequential specification of a location i , $SeqSpec(i)$, is the set of sequential histories of read and write method calls on location i where every read returns the value given as the argument to the latest preceding write (regardless of thread identifiers). It is essentially the sequential specification of a register. Transactional sequential specification is the set of complete sequential transaction histories S that for every transaction T and location i , $Visible(S, T)|i$ is a member of the sequential specification of i . A transaction history H is final-state-opaque if there is an equivalent sequential transaction history S for an extension of H such that S is real-time-preserving and a member of transactional sequential specification. The sequential history S is called the justifying history. In other words, every correct concurrent execution is indistinguishable from a correct sequential execution.