CS 203A Course Syllabus

- Introduction to computer Architecture, Review of Pipelining and Hazards (Appendix A Text)
- Instruction level parallelism, Dynamic scheduling, Branch Prediction and Speculation – Ch 3 Text
- ILP with Software Approaches – Ch 4
- Memory Hierarchy – Ch 5
- VLIW, Multithreading, CMP and Network processor architectures – From papers

Text: Hennessy and Patterson, Computer Architecture: A Quantitative Approach, Morgan Kaufman Publisher

Prerequisite: CS 161 with a grade C or better

Grading

Grading: Based on Curve
Test 1: 30 points
Test 2: 40 points
Project: 30 points