high performance scientific computing

Optimizing Energy Efficiency for Distributed Dense Matrix Factorizations via Utilizing UNIVERSITY OF CALIFORNIA, RIVERSIDE Algorithmic Characteristics Li Tan and Zizhong Chen, UC Riverside Summary Goal: Achieve the optimal energy efficiency for linear algebra operations by using algorithmic characteristics actorized Matrix Pressing demand of improving energy efficiency for Level 2 Panel Matrix BLAS on CPU Scientific apps widely applied on supercomputers Trailing Matrix • Costs of powering supercomputers are increasing Level 3 BLAS G on GPI Strategic DVFS energy efficient scheduling o Switch ↓ hardware power if peak perf. not needed Block Size \circ CPU, GPU, memory \rightarrow Handy APIs for DVFS Numerical linear algebra algorithms: LU, QR, Chol. o Dense Linear Algebra vs. Sparse Linear Algebra Global View o Homogeneous Systems vs. Heterogeneous Sys. Not Yet Processed **Background: Energy Saving Strategies Overall Comparison:** Existing Approaches **Operating Layer** Slack: a period when a hardware waits for another Slack Examples: load imbalance, network latency, OS Level Approaches communication delay, memory and disk access stall • Work aside app. at runtime Critical Path: a particular sequence of tasks where • Make online decisions the total slack amounts to zero in task-parallel apps. • No need of app-specific knowledge o General, no source modification o Enforce hardware to run at the highest F/V when Library Level Approaches workloads are ready and at the lowest F/V otherwise Customize energy saving decisions Utilize app-specific knowledge o Tasks on the CP: Run at highest F/V (peak perf.) Library source modif. & recompilation Optimize potential energy savings Tasks off the CP: Run at appropriately scaled F/V Slack for Elimination by CP (Algorithm 3) Detailed Comparison -----> Dependencies on Different Data Blocks -----> Critical Path o Race-to-halt — → Dependencies on the Same Data Block Slack for Race2Halt by TX (Algorithm 4) Tasks Easy to implement Ρ2 Ρ3 Hardware independent Factorize(1,1) Save additional energy Solve(3,1) Solve(2,1) Solve(4,1) Tter1 due to load imbalance o **CP-aware** Update2(3,3) Update1(4,2) Update2(2,2) Update1(3,2) Update1(4,3) date1(3 Require CP detection Update2(3,3) Update1(4,3)' Factorize(2,2) Hardware dependent Generally the optimal Solve(4,2) Solve(3,2) Iter2 Hardware Utilizat. Polling Update2(3,3) Update1(4,3) Update2(4,4) OS level monitoring Update2(4,4) Factorize(3,3 Coarse-grained Update2(4,4)' Runtime overhead Solve(4,3) Iter3 Update2(4,4)

Critical Path (CP) and Slack Analysis

- CP-free Race-to-halt
- CP-aware Slack Reclamation





o Sleep state scheduling for unused/idle CPU cores

Factorize(4,4)

 Distributed Chol. factorization with 16 x 16 process grid OS level solutions failed to achieve the optimal savings Race-to-halt can be comparable to CP-aware solutions

$$\frac{\binom{3}{i+1}}{\binom{N'^3}{i}} = \frac{\left(N - (i+2) \times N_{proc} \times nb\right)^3}{\left(N - (i+1) \times N_{proc} \times nb\right)^3}$$
$$\operatorname{Eunc}(T_i^U, T_{i+1}^U, param_list)$$