

| R-<br>Type | LW | SW | BEQ | J | Jump | Branch | Reg<br>Dst | ALU<br>Src | Memto-<br>Reg | Reg<br>Write | Mem<br>Read | Mem<br>Write | ALU<br>Op[1] | ALU<br>Op[0] |
|------------|----|----|-----|---|------|--------|------------|------------|---------------|--------------|-------------|--------------|--------------|--------------|
| 1          | 0  | 0  | 0   | 0 | 0    | 0      | 1          | 0          | 0             | 1            | 0           | 0            | 1            | 0            |
| 0          | 1  | 0  | 0   | 0 | 0    | 0      | 0          | 1          | 1             | 1            | 1           | 0            | 0            | 0            |
| 0          | 0  | 1  | 0   | 0 | 0    | 0      | X          | 1          | X             | 0            | 0           | 1            | 0            | 0            |
| 0          | 0  | 0  | 1   | 0 | 0    | 1      | X          | 0          | X             | 0            | 0           | 0            | 0            | 1            |
| 0          | 0  | 0  | 0   | 1 | 1    | 0      | X          | X          | X             | 0            | 0           | 0            | X            | X            |

