

CS120B – Homework #3

Given November 28, 2001. Due December 6, 2001

1. Connect an external 4K EPROM to the 8051 so that the lowest address for the EPROM is at \$6000 hex. All the addresses must be unique. Use only one each of the following ICs: [8051 cpu](#), 8282 latch, [2732 EPROM](#), [74LS138 decoder](#). Refer to the 8051 textbook for information on the 8282 latch and the 8051 cpu.