

Transistors on a chip as seen through an electron microscope

Figure 1 and Figure 2 are pictures taken using an electron microscope looking through the window of an EPROM integrated circuit. Figure 1(a) shows two connection wires from the chip to the package. Connections within the chip can also be seen. Figure 1(b) shows a higher magnification of the rectangle area in (a). Figure 2 shows an area on the IC that is populated with regular patterns of transistors. Each junction in Figure 2 is a transistor inside the chip. Figure 2(b) shows a higher magnification of the rectangle area in (a).

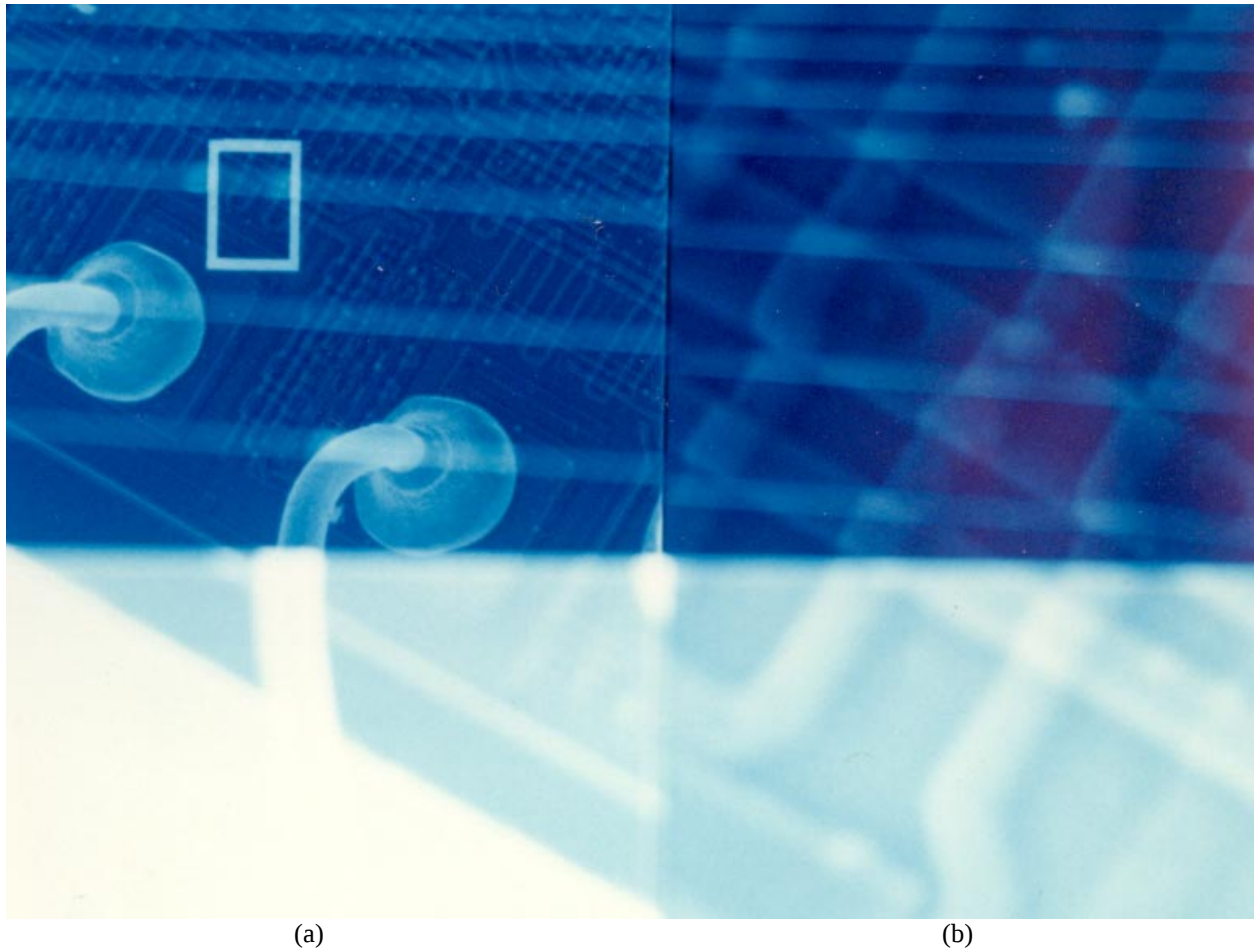


Figure 1. Transistors as seen through an electron microscope: (a) low magnification; (b) high magnification of the rectangle in (a).

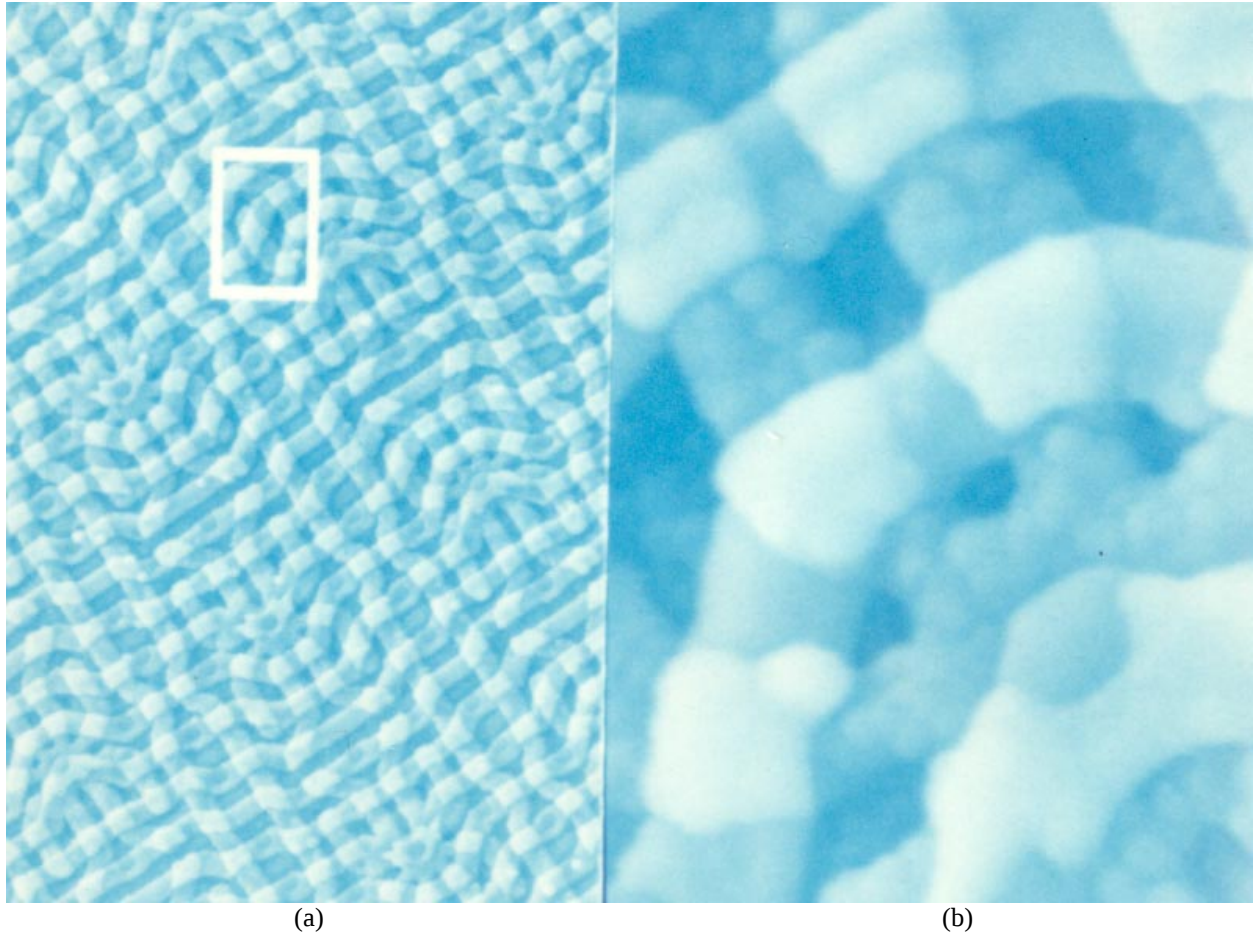


Figure 2. Transistors as seen through an electron microscope: (a) low magnification; (b) high magnification of the rectangle in (a).