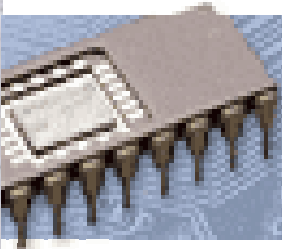
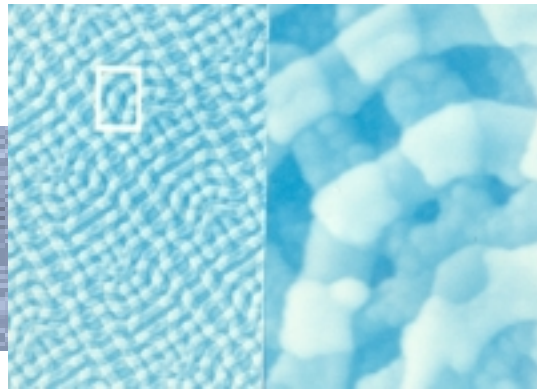




(a)



(b)

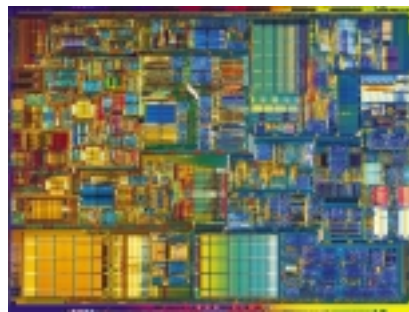


(c)

(d)



(e)



(f)



(g)

**(a) A lump of silicon and a transistor; (b) A 2716 EPROM dual-in-line package; (c) Transistors inside a 2716 EPROM as seen through an electron microscope: low magnification; (d) high magnification of the rectangle area in (c); (e) Intel Pentium 4 and chipset; (f) Intel Pentium 4 die (internal circuit); (g) Circuit board.**