UNIVERSITY OF CALIFORNIA, RIVERSIDE

Department of Computer Science and Engineering

Department of Electrical Engineering

CS/EE120A – Logic Design

Homework 3

Given June 1, Due June 8, 2001

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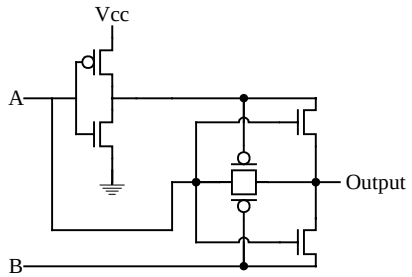
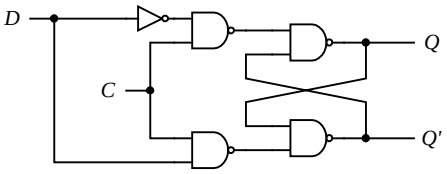
1. Determine the truth table for the following CMOS circuit (5)



2. Determine the truth table for the following sequential circuit (5)