

Implicit representations and algorithms for the logic and stochastic analysis of discrete–state systems^{*}

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As discrete–state systems are pervasive in our society, it is essential that we model and analyze them effectively, both prior to putting them in operation and during their useful life. The size of their state space, however, is a huge obstacle in practice. Often, the “easy” way to tackle this problem is to use some type of simulation, but this technique has obvious limitations. For performance analysis, simulation can at best offer only a statistical approximation, i.e., confidence intervals, while, for logic analysis, the situation is even worse, as it can only find errors, not prove correctness. Ultimately, these limitations stem from the same source: simulation only visits a fraction of the reachable states. Indeed, the fraction of the states that can actually be explored in a reasonable amount of time becomes exponentially smaller as the complexity of the system being modeled (measured in number of components, parts, etc.) increases.

One way to attack this problem is to employ *implicit representations* whose memory and time requirements are often much less than linear in the number of states of the system under study. In this context, two techniques emerged in the mid ’80s are particularly relevant.

- Since the introduction of *binary–decision diagrams* (BDDs) [1], *symbolic* algorithms have proven themselves very effective for the verification of discrete–state systems, especially digital hardware and protocols. Systems with 10^{20} or more states have become amenable to exact logic analysis, leading to the wide adoption of *model checking* by major hardware and software vendors.
- In an apparently distant area of research, the structure of a Markovian discrete system has been exploited to develop a *Kronecker algebra* representation of the infinitesimal generator matrix of the system [11]. By avoiding the need to store this matrix, which is the largest data structure required for a steady–state or transient analysis of a Markov model, systems with a factor of 10 or more states can be studied than with ordinary sparse–storage methods. While this improvement is not as impressive as that of symbolic model checking, the idea has spurred much research and further advancements in the two decades following its introduction.

^{*} Work supported in part by the National Science Foundation under grants CNS-0501747 and CNS-0501748.

BDDs, Kronecker representations, and several other variants that have been introduced in the literature can all be seen as *implicit* representations of vector or matrices, as opposed to *explicit* representations; the fundamental difference between the two being that, when using the latter, the memory, hence time, requirements are always at least proportional to the number of (nonzero) entries in the vector or matrix being stored, while the former is enormously more efficient in many cases.

In this talk, we first survey and organize several types of decision diagrams which can be employed for logic or Markov analysis, and briefly illustrate important algorithms that manipulate them, from fixed-point algorithms used in state-space generation [3, 7] and CTL model checking [6] to vector-matrix multiplications [2] needed for the numerical solution of a Markov model: *multiway decision diagrams* (MDDs) [12], *multi-terminal decision diagrams* (MTBDDs, MTMDDs) [8], *additive or multiplicative edge-valued decision diagrams* (EVBDDs, EV⁺MDDs and EV*⁺MDDs) [9, 5], *matrix diagrams* (MxDs) [4, 10], and a new variant particularly efficient when storing matrices, *identity-reduced decision diagrams*.

Then, we focus on the need for approximate solutions of Markov models that have been encoded implicitly. This is an important challenge to be tackled, because, while we can analyze the logic behavior and even encode the infinitesimal generator of systems with huge state spaces, the exact numerical solution of the underlying Markov model still requires the storage of an explicit probability vector in practice. We present some known results, and discuss potential research directions.

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