CS 203A Course Syllabus, winter 2012
T, Th 3.40-5.00 pm, Room ENGR2 139

Text: Computer Architecture: A Quantitative Approach
By Hennessy and Patterson, 5th Edition

• Introduction to computer Architecture, Performance (Chapter 1)
• Review of Pipelining, Hazards, Branch Prediction (Appendix C)
• Memory Hierarchy Design (Appendix B and Chapter 2)
• Instruction level parallelism, Dynamic scheduling, and Speculation (Appendix C and Chapter 3)
• Multiprocessors and Thread Level Parallelism (Chapter 5)

Prerequisite: CS 161 or consent of the instructor

Grading
Grading: Based on Curve
Test 1: 35 points
Test 2: 35 points
Project 1: 15 points
Project 2: 15 points

Instructor
Office Times: W, 3-5 pm
Laxmi Narayan Bhuyan
Office: Engg.II Room 351
E-mail: bhuyan@cs.ucr.edu
Tel: (951) 787-2244

TA: Li Yan
Office Hours: Tuesday 1-3 pm

Cell: (951)823-3326
email: lyan@cs.ucr.edu